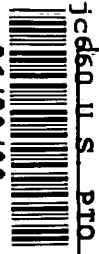


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Jc660 U.S. PTO

☒ "Express Mail" mailing label No. EJ 338 395 120 US Date of Deposit JUNE 9, 1999

I hereby certify that this paper is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

RICHARD S. ROBERTS

PATENT APPLICATION TRANSMITTAL FORM	DOCKET NO. (Optional)
	30-4718 (4780)

To the Assistant Commissioner for Patents:

Transmitted herewith for filing is the patent application of HENRY CHUNG



entitled A FABRICATION METHOD OF INTEGRATED CIRCUITS WITH BORDERLESS VIAS AND LOW DIELECTIC-CONSTANT INTER-METAL DIELECTRICS

Enclosed are: 31 Pages of Specification, Claims and Abstract

- ☒ 37 sheets of drawings.
- ☐ An assignment recordation cover sheet and an assignment of the invention to: \_\_\_\_\_
- ☒ Declaration of the inventors. NOT Executed
- ☐ a certified copy of a \_\_\_\_\_ application.
- ☐ associate power of attorney.
- ☐ a verified statement to establish small entity status under 37 CFR 1.9 and 1.27.
- ☒ RULE 56 information disclosure statement, PTO 1449, 2 References
- ☐ preliminary amendment.
- ☒ other: RULE 54 LETTER

	NUMBER FILED	NUMBER EXTRA	RATE	FEE
BASIC FEE			\$ 760.00	\$ 760.00
TOTAL CLAIMS	16-20=	0	\$ 18.00	\$0.00
INDEPENDENT CLAIMS	4-3=	1	\$ 78.00	\$78.00
MULTIPLE DEPENDENT CLAIM PRESENT				
*NUMBER EXTRA MUST BE ZERO OR LARGER			<b>TOTAL</b>	<b>\$838.00</b>

☒ The Assistant Commissioner is hereby authorized to charge and credit Deposit Account No. 01-1125 as described below. I have enclosed a duplicate copy of this sheet.

- ☒ Charge the amount of \$ 838.00 as filing fee.
- ☒ Charge any additional filing fees required under 37 CFR 1.16 and 1.17 or credit any overpayment.

JUNE 9, 1999  
Date

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:  
HENRY CHUNG

Docket: 30-4718 (4780)

Serial Number:

Group Art Unit:

Filed:

Examiner:

For: A FABRICATION METHOD OF INTEGRATED CIRCUITS WITH  
BORDERLESS VIAS AND LOW DIELECTRIC-CONSTANT INTER-METAL  
DIELECTRICS

LETTER PURSUANT TO 37 C.F.R. 1.54

Assistant Commissioner for Patents  
Washington, D.C. 20231

Enclosed herewith is the specification for the above captioned patent application. The inventor's Declaration and Power of Attorney will be filed shortly to complete the application pursuant to 37 C.F.R. 1.53. It is therefore respectfully requested that the application be accorded a filing date of JUNE 9, 1999 in conformance with Patent Office Rules 10, 53 and 54.

Respectfully submitted,



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